

alloy or Al containing metal such as Al and Al alloy. The data wires may have a multilayered structure including a low resistivity conductive layer.

The first data wire includes a plurality of first data lines 171 extending in the longitudinal direction and intersecting the gate lines 121 to define a plurality of pixel areas, a plurality of first source electrodes 173 connected to the first data lines 171 and to the source regions 1403 of the switching semiconductor portions 140 through the contact holes 1803, and a plurality of first drain electrodes 175 separated from the first source electrodes 173 and connected to the drain regions 1405 of the switching semiconductor portions 140 through the contact holes 1805 and to the source regions 1423 of the driving semiconductor portions 142.

The second data wire includes a plurality of second data lines 172 extending substantially in the longitudinal direction and overlapping the storage electrodes 124, a plurality of second source electrodes 174 connected to the second data lines 172 and to the source regions 1423 of the driving semiconductor portions 142 through the contact holes 1823, and a plurality of second drain electrodes 176 separated from the second source electrodes 174 and connected to the drain regions 1425 of the driving semiconductor portions 142 through the contact holes 1825.

Although it is not shown in the figures, the first and the second data wires may include a plurality of data pads connected to the first and the second data lines 171 and 172 for transmitting electrical signals from an external source to the first and the second data lines 171 and 172.

A second interlayer insulating layer 185 having a plurality of contact holes 1855 exposing the second drain electrodes 176 is formed on the data wire 171, 172, 173, 174, 175 and 176, and a plurality of pixel electrodes 192 preferably made of transparent material such as indium tin oxide (ITO) and indium zinc oxide (IZO) are formed on the second interlayer insulating layer 185. The pixel electrodes 192 are connected to the second drain electrodes 176 through the contact holes 1855 of the second interlayer insulating layer 185.

Although it is not shown in the figures, the TFT array panel for an EL display according to the embodiment of the present invention further includes a plurality of red, green and blue organic light emission members formed on the